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	Filing Date		2004-01-09	
	First Named Inventor	Mou-Shiung Lin		
	Art Unit	2815		
	Examiner Name	JACKSON JR, JEROME		
	Attorney Docket Number	MEGP0004USA1		

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1	JACOB MILLMAN, Digital and Analog Circuits and Systems, MICRO ELECTRONIC, March 1979, p 115 & p 167, McGraw-Hill College.	<input type="checkbox"/>
2	Rao R. Tummala, Eugene J.Rymaszewski & Alan G. Klopfenstein, Technology Drivers Part I, MICROELECTRONICS PACKAGING HANDBOOK (Second Edition), January 31, 1997, p.12~13, p.64~65, p.82~87, p.133, An overview & 8-2 chip-level interconnection evolution, Springer.	<input type="checkbox"/>
3	John H. Lau & S.W. Ricky Lee, Design Concepts and Package Structure, Chip Scale Package, February 28, 1999, p.157~161, Chapter 10.2, McGraw-Hill Professional.	<input type="checkbox"/>

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